

IN THE SPECIFICATION

The abstract has been amended to not exceed 150 words as requested by the Examiner.

Please replace paragraph [0004] with the following amended paragraph:

[0004] Digital cameras typically capture images electronically and ultimately store the images as bits (one and zeros) on a solid-state memory. Flash memory is the most common storage for digital cameras. Flash memory contains one or more electrically-erasable programmable read-only memory (EEPROM) integrated circuit chips that allow reading, writing, and block erasing.

Please replace paragraph [0027] with the following amended paragraph:

As illustrated in FIG. 2, USB connector 111 connects controller 200 to the host computer. In addition to a standard bus 201 for controlling the slots 102a-n, controller 200 has a multitude of multiplexed control lines 202 a-n. In one embodiment, the lines 202 a-n address multiplexors, or transfer buffers, 220 a-n, which in one embodiment may turn on/off selected ~~lines~~ control lines 212a-n and/or data lines 211a-n to activate/deactivate or communicate data with the flash media in each respective slot(s).

Amendment to the Abstract

This new version will replace all prior Abstract versions in the application:

Please replace paragraph [0034] with the following amended paragraph:

A memory card reading apparatus is described including a plurality of memory card interfaces, with at least of subset of the plurality memory card interfaces configured to interface with a memory card of a first type, the plurality of memory card interfaces accessible in parallel. In one embodiment, at least on of the memory card interfaces is configured to interface with a plurality of different memory card types. In another embodiment, a first subset of the plurality of memory card interfaces are configured to interface with a memory card of a first type and a second subset of the plurality of memory card interfaces are configured to interface with a memory card of a second type, wherein the first and second subset of memory card interfaces are accessible in parallel. ~~In yet another embodiment, at least one of the memory card interfaces includes an indicator identifying a status of an operation for a respective memory card interface.~~